		STUDY MODULE D	ESCRIPTION FORM	_
	f the module/subject oprocessor Tech	nology		Code 1010642221010322511
Field of	-		Profile of study	Year /Semester
Mec	hanical Engineer	ing	(general academic, practical (brak)	¹⁾ 1/2
	path/specialty		Subject offered in:	Course (compulsory, elective)
	Ν	Nechatronics	Polish	obligatory
Cycle of	f study:		Form of study (full-time,part-time))
	Second-c	ycle studies	full-	time
No. of h	ours			No. of credits
Lectur		,	Project/seminars:	1 2
Status o	-	program (Basic, major, other)	(university-wide, from another	
Education	on areas and fields of sci	(brak) ence and art		(brak) ECTS distribution (number
				and %)
techr	nical sciences			2 100%
	Technical scie	ences		2 100%
ema tel. (Wyo ul. F	nž. Grzegorz Trzmiel ail: Grzegorz.Trzmiel@ 61 665 2693 dział Elektryczny Piotrowo 3A, 60-965 P cquisites in term		d social competencies	
1	Knowledge	Elementary knowledge of electri	cal engineering, electronics ar	nd digital technology.
		Ability to:		
2	Skills	 acquiring, integrating and interp - interpretation, documentation a project, 	•	
		- formulating algorithms for simp		
3	Social competencies	Thinking and acting in an entrep social diversity opinions.	reneurial manner while mainta	aining professional ethics and
Assu	mptions and obj	ectives of the course:		
		eoretical problems associated with sor systems and the basis of their		rinciple components, sub-
	Study outco	mes and reference to the	educational results for	r a field of study
Know	vledge:			
		oth knowledge of digital technolog		_W01]
		oth knowledge of control systems.		
3. He h [K2A_\		e of the components and subasse	mblies included in the digital d	levices and microprocessor
-	has knowledge of the p	principles of design of microproces	sor control systems exemplar	y processes and devices
Skills	-			
		fy a critical evaluation and opinior	is [K2A_U08]	
	-	a presentation and lead a discussion		ask [K2A_U16]
	an plan and execute t	he processes of design, simulatio		
Socia	al competencies:			
1. He ι	inderstands the need	to think and act creatively and ent	erprising, including working to	gether as a team [K2A_K03]
2. He is	s aware of the validity	of the transfer of knowledge and s	solutions through the media	[K2A_K06]

Assessment methods of	study outcomes			
Lecture: written tests, presentations own concept solutions, written ex	am / oral examination.			
Course descri	ption			
Lecture: The idea of pipelining. Architecture microprocessors. Constru functionality of a microcontroller. Microcontrollers closed (embedded). signal distribution systems. Methods for power reduction. Special mor supervising the correct operation of the microcontroller. Watchdog. M interrupts. Programming nested. Basic programming languages. Com features, systems, types of frames (without detailed structures), mode concepts construction node, electromagnetic interference advantages	. The microprocessor core. The des microcontroller. RESET. S ethods of cooperation with per missioning and testing prograr el of communication, error dete	e oscillator and clock ources RESET. Systems ipherals. Systems ns. CAN interface:		
Project: Getting to know the architecture of an exemplary microcontro handling internal and external devices. Basics of C51 language special internal systems, among others, timers and interrupt system, serial, A among others, LCD, LED, matrix keyboard. Implementation of the exe an external device.	fication, implementation progra C transducer. Implementation	ams, use of selected of external devices,		
Basic bibliography:				
1. Jabłoński T., Pławsiuk K., Programowanie mikrokontrolerów PIC w	języku C, BTC, Warszawa 200	05.		
2. Krzyżanowski R., Układy mikroprocesorowe, Mikom, Warszawa 2004.				
3. Pietraszek S., Mikroprocesory jednoukładowe PIC, Wyd. Helion, G				
Additional bibliography:				
1. Jabłoński T., Mikrokontrolery PIC16F8x w praktyce, Wyd. BTC, Wa	arszawa. 2002.			
2. Francuz T., Język C dla mikrokontrolerów, od podstaw do zaawans		wice 2011,		
3. Prace dyplomowe IEiEP				
4. Internet.				
Result of average stude	ent's workload			
Activity		Time (working hours)		
1. Participation in class lecture				
2. Consultation on the lecture		13		
2. Dreparation for discussion (active form) in last una	13 2			
 Preparation for discussion (active form) in lectures 		-		
		2		
4. Preparation to pass		2 4		
4. Preparation to pass 5. Pass of the lecture		2 4 4		
4. Preparation to pass5. Pass of the lecture6. Participation in projects		2 4 4 2		
 Preparation to pass Pass of the lecture Participation in projects Preparation for projects 		2 4 4 2 13		
 Preparation to pass Pass of the lecture Participation in projects Preparation for projects Preparation for pass the laboratory 		2 4 4 2 13 6		
 4. Preparation to pass 5. Pass of the lecture 6. Participation in projects 7. Preparation for projects 8. Preparation for pass the laboratory 	kload	2 4 4 2 13 6 6		
 4. Preparation to pass 5. Pass of the lecture 6. Participation in projects 7. Preparation for projects 8. Preparation for pass the laboratory 9. Pass of the project 	kload hours	2 4 4 2 13 6 6		
 4. Preparation to pass 5. Pass of the lecture 6. Participation in projects 7. Preparation for projects 8. Preparation for pass the laboratory 9. Pass of the project Student's worl Source of workload	hours	2 4 4 2 13 6 6 2 ECTS		
 6. Participation in projects 7. Preparation for projects 8. Preparation for pass the laboratory 9. Pass of the project Student's worl		2 4 4 2 13 6 6 2		